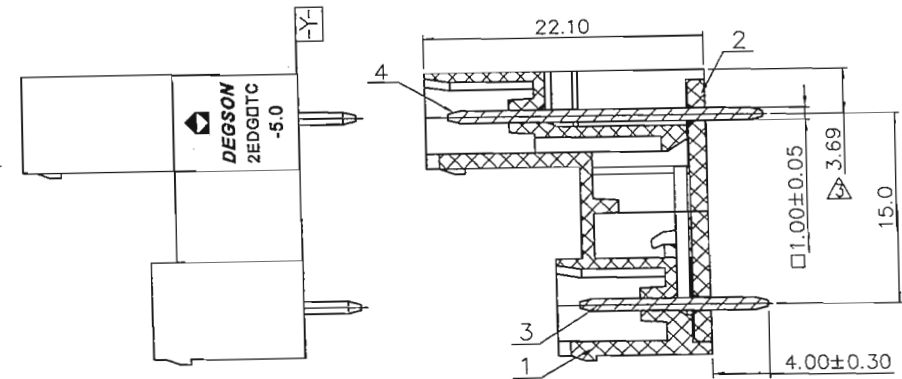
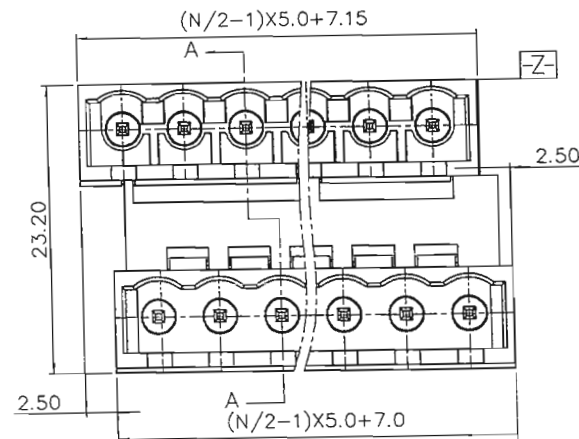


D1RFRD006

active

REVISION RECORD

REV	ECN	DESCRIPTION	DRFT	DATE
△		品名、料号增加环保标示“(H)”	LJH	12/11/07
△		增加VDE刻字	Zengy	11/12/08
△	14-0107	黄铜变紫铜, 料号变更; 工频耐压变更为额定冲击电压, 加尺寸变更	HHJ	12/17/14



SEC A-A

NOTES:

- 1.DIMENSION SHALL BE INTERPRETED PER ASME Y14.5-2009.
- 2.PITCH: 5.0MM ,POLES: ONE-BLOCK FOR 4-24P
INTER LOCKING FOR 24-48P
- 3.RATED VOLTAGE/CURRENT: UL 300V/15A
VDE 320V/10A
- 4.OPERATION TEMPERATURES: -40℃~+105℃
- 5.RATED IMPULSE VOLTAGE: 4.0KV
- 6.INSULATION RESISTANCE: 5000MΩ/DC1000V
- 7.UNDIMENSIONED TOLERANCES:

Pitch range in mm			Nominal dimension range in mm					
to 6	over 6 to 10	over 10 to 24	to 30	over 30 to 60	over 60 to 100	over 100 to 150	over 150	
±0.10	±0.15	±0.25	±0.2	±0.3	±0.5	±0.7	±1.0	0.05/10

P.C.B LAYOUT

△	4	2EDGVT-long pin	1032B00040(H)	XX/2	COPPER	Sn Plated
△	3	2EDGV-pin	1007B00330(H)	XX/2	COPPER	Sn Plated
		2EDGV-5.0-Cover	1032C0Y(1/2+1)A(H)	1	PA66	>24P
2		2EDGV-5.0-Cover	1032C0Y(1/2-1)A(H)	1	PA66	>24P
		2EDGV-5.0 Cover	1032C0YXXA(H)	1	PA66	≤24P

1	2EDGVT-5.0 housing(Right foot)	1072A0Y(1/2+1)C(H)	1	PA66	>24P
	2EDGVT-5.0 housing(Left foot)	1072A0Y(1/2-1)B(H)	1	PA66	>24P
	2EDGVT-5.0-Housing	1072A0YXXA(H)	1	PA66	≤24P

ITEM	NAME OF PART	NO. OF PART	Q'TY	MATERIAL	NOTES
MM (INCH) TOLERANCES EXCEPT AS NOTED .X ± .XX ± .XXX ± MM (INCH) TOLERANCES EXCEPT AS NOTED .X ± .XX ± .XXX ±					
ANGLES ± 1° THIRD ANGLE PROJECTION MATERIAL: FINISH: PART NO.: NAME: NAME:					
DEGSON DEGSON ELECTRONICS CO., LTD 2EDGVT-5.0-XXP-1Y-00A(H)					
APPD: <i>Aaron 12/17/14</i> CHKD: <i>Xm 12/17/14</i> DRFT: Apple 06/21/06 TITLE: ASSEMBLY DRAWING DRAWING NO.: 2EDGVT-5.0-E01 DO NOT SCALE DRAWING REV T SHEET					